

## **ABSTRACT OF THE DISCLOSURE**

A memory card having a plurality of different interfaces is disclosed. The memory card comprises a function module, a controller coupled to the function module for accessing, a first buffer coupled to the controller for sending a first control signal, and a second buffer coupled to the controller for sending a second control signal. The memory card activates one interface for detecting a first voltage level of the first buffer when the memory card is inserted into a first card reader, and the first buffer is activated to send the first control signal if the detection is positive.

5 Alternatively, the memory card activates the other interface for detecting a second voltage level of the second buffer when the memory card is inserted into a second card reader, and the second buffer is activated to send the second control signal if the detection is positive.

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